

Notice of Allowability

Application No.

10/523,900

Examiner

Longbit Chai

Applicant(s)

KORNBLUM, CHRISTIAN

Art Unit

2131

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/5/2007.
2. ☒ The allowed claim(s) is/are 13-25.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

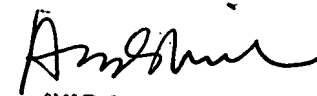
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date 11/6/2007.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


AYA7 SHEIKH
SUPERVISORY PATENT EXAMINER
TECHNICAL CHAIRMAN 2109
Part of Paper No./Mail Date 2007 1107

DETAILED ACTION

Examiner's Amendment

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this Examiner's Amendment was given in a telephone interview with Aaron C. Deditch (reg. no. 33,865) on 6 November 2007.

This application has been amended as follows:

IN THE CLAIMS

Replace claims 13 and 23.

Claim 13:

A method for transcribing at least one data record of an external data source to a processor unit, comprising:

transmitting the at least one data record from the external data source together with additional information to a buffer memory of the processor unit, the additional information including an identifier assigned individually to the processor unit;

performing, in the processor unit, a check of an admissibility of a use of the at least one data record based on the additional information;

generating a blocking signal when the check indicates that the use of the at least one data record is not allowed;

deleting the at least one data record from the buffer memory; and

generating an enable signal when the use of the at least one data record is allowed;

~~comparing a code word from a code word memory having a stored list of code words, the code word memory being of the processor unit; and~~

comparing the identifier, transmitted together with the at least one data record, with a code word from a code word memory having a stored list of code words, the code word memory being of the processor unit to determine whether the use of the at least one data record at the processor unit is allowed, wherein with each check of the identifier in the processor unit another code word is used and therefore each code word identifier is valid only once and no longer available for use.

Claim 23:

A processor unit, comprising:

a buffer memory;

a rewritable functional memory that is accessible during an operation of the processor unit, the buffer memory and the rewritable function memory being capable of storing at least one data record;

an interface for importing the at least one data record and additional information into the buffer memory; and

a check unit for checking a validity of the at least one data record;

a transmitting arrangement to transmit the at least one data record from the external data source together with additional information to a buffer memory of the processor unit, the additional information including an identifier assigned individually to the processor unit;

a checking arrangement to check an admissibility of a use of the at least one data record based on the additional information;

a generating arrangement to generate a blocking signal when the check indicates that the use of the at least one data record is not allowed; deleting the at least one data record from the buffer memory; and to generate an enable signal when the use of the at least one data record is allowed; and

a comparing arrangement to compare the identifier, transmitted together with the at least one data record, with a code word from a code word memory having a stored list of code words, the code word memory being of the processor unit to determine whether the use of the at least one data record at the processor unit is allowed, wherein with each check of the identifier in the processor unit another code word is used and therefore each code word identifier is valid only once and no longer available for use

~~wherein in a code word memory of the processor unit a list of code words is stored, the identifier being compared with the code word, and wherein with each check of the identifier in the processor unit another code word is used and thus also spent.~~

Allowable Subject Matter

Claims 13 – 25 are allowed.

The following is an examiner's statement of reasons for allowance:

The above mentioned claims are allowable over prior arts because the CPA (Cited Prior Art) of record fails to teach or render obvious the claimed limitations in combination with the specific added limitations, as recited in independent claims 13 and 23.

The prior arts fail to teach or suggest method for transcribing at least one data record of an external data source to a processor unit that comprises transmitting the at least one data record from the external data source together with additional information to a buffer memory of the processor unit, the additional information including an identifier assigned individually to the processor unit; performing, in the processor unit, a check of an admissibility of a use of the at least one data record based on the additional information; generating a blocking signal when the check indicates that the use of the at least one data record is not allowed; deleting the at least one data record from the buffer memory; and generating an enable signal when the use of the at least one data record is allowed; comparing the identifier, transmitted together with the at least one data record, with a code word from a code word memory having a stored list of code

words, the code word memory being of the processor unit to determine whether the use of the at least one data record at the processor unit is allowed, wherein with each check of the identifier in the processor unit another code word is used and therefore each code word identifier is valid only once and no longer available for use.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Longbit Chai whose telephone number is 571-272-3788. The examiner can normally be reached on Monday-Friday 8:00am-4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz R. Sheikh can be reached on 571-272-3795. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.


Application/Control Number:
10/523,900
Art Unit: 2131

Page 7

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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LBC


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